

AMENDMENTS TO THE SPECIFICATION:

Please replace the paragraph beginning at page 2, line 21 with the following amended paragraph:

An image processing apparatus according to a first aspect of the invention is provided with: a pixel matrix formation section ~~consist of a device that has~~ having a rewritable circuit configuration, and having a plurality of line memories that output pixel data in parallel; a filtering circuit ~~consist of a device that has~~ having a rewritable circuit configuration, and performing filtering of pixel data by use of a pixel matrix based on the pixel data received in parallel from the line memories; a memory for storing setting information for rewriting the configurations of the devices pixel matrix formation section and the filtering circuit; and a controller for rewriting the configuration of the line memories and the configuration of the filtering circuit by use of the setting information stored in the memory based on an image processing condition.

Please replace the paragraph beginning at page 3, line 19 with the following amended paragraph:

An image processing apparatus according to a third aspect of the invention is provided with: a first circuit ~~consist of~~ comprising a device that has a rewritable configuration, and having a plurality of line memories; a second circuit for processing image data output from the line memories; a memory for storing setting information for rewriting the configuration of the first circuit; and a controller for rewriting the configuration of the line memories of the first circuit by use of the setting information stored in the memory based on an image processing condition.